

Figure 13.22 Schematic description of the manufacturing steps to make monolithic interconnections for thin-film Cu(InGa)Se_2 PV modules

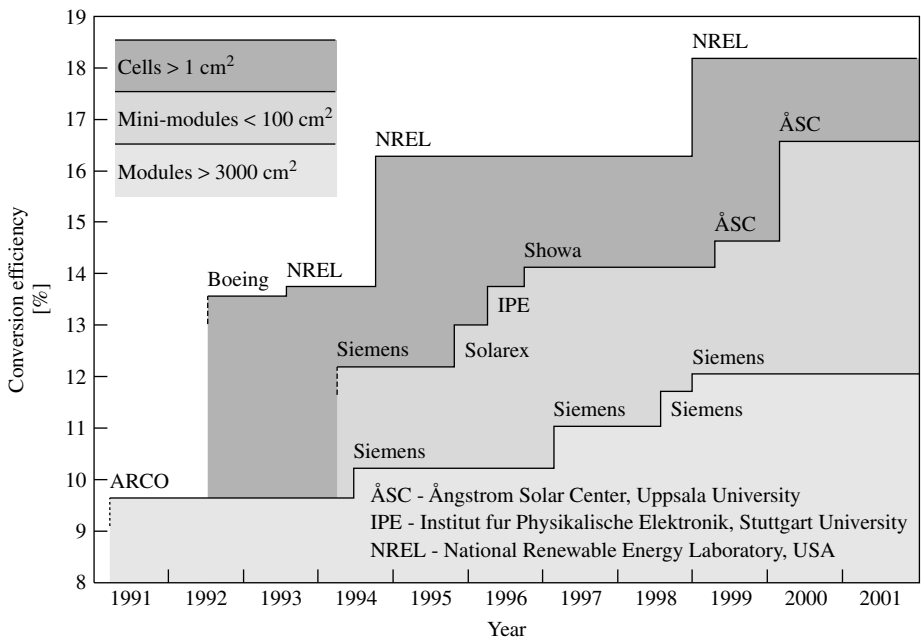


Figure 13.23 Evolution of Cu(InGa)Se_2 device record efficiencies in the past decade. All data are taken from the Solar Cell Efficiency Tables periodically published in *Progress in Photovoltaics*