Important Note
HD I/O Option Board Owner's Guide Errata

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The Silicon Graphics *HD I/O Option Board Owner's Guide* (007-3968-001) has the following errors:

- In the manual, all illustrations of the board panel show LED silkscreens **Locked** and **Present**. These should be **LOCKED** and **VALID**, as shown below.
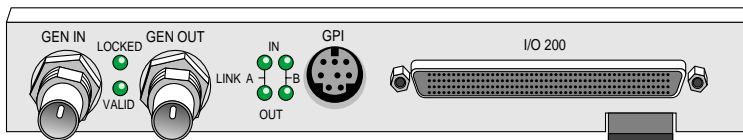
Illustrations of the board panel are on page 6 in Chapter 1, pages 46 and 50 in Appendix A, and page 51 in Appendix B.

- In Chapter 2, the section "Field Dominance" contains errors: Figure 2-6 is incorrect, as is the second paragraph after it. The correct version of the figure is shown below.
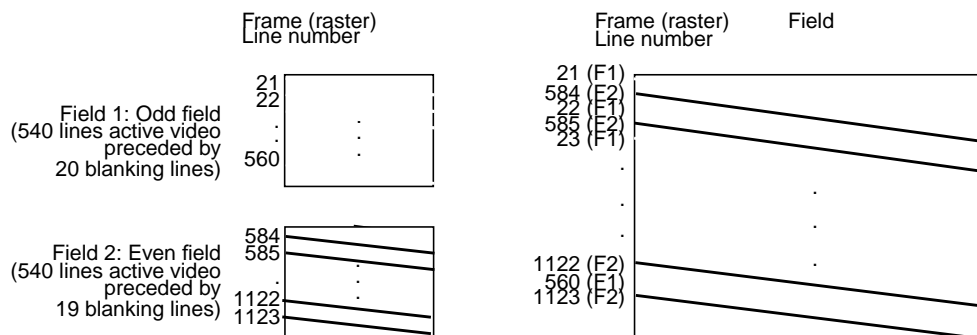
Figure 2-6   Fields and Frames for SMPTE 274

The paragraphs after the figure should read as follows:

Editing is usually on Field 1 boundaries, where Field 1 is defined as the first field in the video standard's two-field output sequence. However, some users may want to edit on F2 boundaries, which fall on the field in between the video standard's frame boundary. To do so, use this control, then program your deck to select F2 edits.

A set of frames to be output must be deinterlaced into fields differently, depending on the choice of output field dominance: for SMPTE 274M, the top line is in F1, as shown in Figure 2-6; for SMPTE 240M, the top line is in F2. For example, when F1 dominance is selected, the field with the topmost line must be the first field to be transferred; when F2 dominance is selected, the field with the topmost line must be the second field to be transferred.

- Table A-3 in Appendix A is incorrect. The correct version is shown below.

Table A-3   Usage for LINK A and LINK B in 4:2:2:4 Mode

Sample	LINK A	LINK B
0	Cb0 Y0	x A0
1	Cr1 Y1	x A1
2	Cb2 Y2	x A2
3	Cr3 Y3	x A3

- Table C-1 in Appendix C contains extraneous material in the leftmost column: the numbers *5* and *10*, and the word *NEW*.