



## Unified Parallel C (UPC) User's Guide

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## Record of Revision

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## About This Manual

This publication documents the SGI implementation of the Unified Parallel C (UPC) parallel extension to the C programming language standard.

### Obtaining Publications

You can obtain SGI documentation in the following ways:

- See the SGI Technical Publications Library at: <http://docs.sgi.com>. Various formats are available. This library contains the most recent and most comprehensive set of online books, release notes, man pages, and other information.
- You can also view man pages by typing `man title` on a command line.

### Related Publications and Other Sources

Material about UPC is available from a variety of sources. Some of these, particularly webpages, include pointers to other resources. Following is a list of these sources:

- *UPC: Distributed Shared Memory Programming*  
Authors: Tarek El-Ghazawi, William Carlson, Thomas Sterling, Katherine Yelick;  
ISBN: 0-471-22048-5 ; Published by John Wiley and Sons- May, 2005
- <http://upc.gwu.edu>  
Contains much information that is relevant to UPC.
- [http://upc.gwu.edu/docs/upc\\_specs\\_1.2.pdf](http://upc.gwu.edu/docs/upc_specs_1.2.pdf)  
Contains the description of Version 1.2 of the UPC programming language.
- <http://upc.gwu.edu/downloads/Manual-1.2.pdf>  
Contains a discussion about the UPC language features.
- `sgiupc(1)` man page  
SGI Unified Parallel C (UPC) compiler man page describes the `sgiupc(1)` command. `sgiupc` is the front-end to the SGI UPC compiler suite. It handles all

stages of the UPC compilation process: UPC language preprocessing, UPC-to-C translation, back- end C compilation, and linking with UPC runtime libraries.

## Conventions

The following conventions are used throughout this document:

<b>Convention</b>	<b>Meaning</b>
<code>command</code>	This fixed-space font denotes literal items such as commands, files, routines, path names, signals, messages, and programming language structures.
<code>manpage(x)</code>	Man page section identifiers appear in parentheses after man page names.
<i>variable</i>	Italic typeface denotes variable entries and words or concepts being defined.
<b>user input</b>	This bold, fixed-space font denotes literal items that the user enters in interactive sessions. (Output is shown in nonbold, fixed-space font.)
[ ]	Brackets enclose optional portions of a command or directive line.
...	Ellipses indicate that a preceding element can be repeated.

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## Introduction

The *UPC Language Specifications* document defines Unified Parallel C (UPC) as a parallel extension to the C programming language standard that follows the partitioned global address space programming model. It is available at the following location: [http://upc.gwu.edu/docs/upc\\_specs\\_1.2.pdf](http://upc.gwu.edu/docs/upc_specs_1.2.pdf).

*UPC: Distributed Shared Memory Programming* provides information about UPC programming language. For details about this manual and other resources related to UPC, see the preface “About This Manual”.

The UPC common global address space (SMP and NUMA) provides an application with a single shared, partitioned address space, where variables may be directly read and written by any processor, but each variable is physically associated with a single module load `sgi-upc-develprocessor`.

This manual documents the SGI implementation of the UPC standard.

## UPC Implementation

The SGI implementation of UPC conforms to Version 1.2 standard. Parallel I/O, which is not yet a part of the language, is not supported.

The SGI Unified Parallel C (UPC) compiler man page describes the `sgiupc(1)` command. `sgiupc` is the front-end to the SGI UPC compiler suite. It handles all stages of the UPC compilation process: UPC language preprocessing, UPC-to-C translation, back-end C compilation, and linking with UPC runtime libraries.

To see the `sgiupc(1)` man page, make sure the `sgi-upc-devel` module is loaded, as follows:

```
% module load sgi-upc-devel
```

## Compiling and Executing a Sample UPC Program

A sample UPC program (`hello.c`) is, as follows:

```
#include <upc.h>
#include <stdio.h>
int
```

```
main ()
{
    printf("Executing on thread %d of %d threads\n", MYTHREAD, THREADS);
}
```

To compile this program and generate the executable `hello`, use the following command:

```
# sgiupc hello.c -o hello
```

The `mpirun(1)` command is used for execution. If you want the program to execute using four threads, perform the following command:

```
# mpirun -np 4 hello
```

You can expect output similar to the following:

```
Executing on thread 1 of 4 threads
Executing on thread 3 of 4 threads
Executing on thread 0 of 4 threads
Executing on thread 2 of 4 threads
```

---

**Note:** The statements might not appear in the order listed in the output example, above.

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For more information on `sgiupc(1)` and `mpirun(1)`, see the corresponding man pages.

## Mixing of UPC Programs with Other Languages

The rules for mixing UPC programs with programs written in other languages are similar to that of mixing a C program compiled with the native compiler used to compile the UPC program (as specified by `UPC_NATIVE_CC`), with the caveat for shared pointers, as follows:

If the main program is compiled using `sgiupc`, the appropriate libraries needed for running UPC programs are linked in. If the main program is not a UPC program compiled with `sgiupc`, the appropriate runtime libraries needed by `sgiupc` have to be explicitly linked in. You can determine this by specifying the `-v` option to the `sgiupc` command used to compile and link an application comprising of a single UPC program.

## Shared Pointer Representation and Access

In order to handle large thread counts, as well as large blocking size, the SGI UPC compiler uses a `struct` type to represent a shared pointer. As SGI reserves the right to change this representation at a later time, it would be best to use UPC provided functions to access the individual components if a shared pointer is to be passed to a non-UPC function.

## Vectorization of Loops to Reduce Remote Communication Overhead

Consider the following loop:

```
upc_forall (i = 0; i < N; i++; i)
    a[i] = b[i] + c[i];
```

If the array references are all remote, there are  $2*N$  remote loads and  $N$  stores performed in this loop.

If the loop does not have any aliasing issues, the number of remote loads can be reduced to 2 and the stores to 1, although each of these would be dealing with  $N$  elements at a time. This will cut down the communication overheads to fetch remote data.

If `a`, `b`, and `c` are shared restricted pointers, the compiler is able to figure out that there are no aliasing issues, and it is able to vectorize this loop so that remote block data accesses can be used.

For all other cases, the user can specify a `pragma` type before the loop, as follows:

```
#pragma sgi_upc vector=on
upc_forall (i = 0; i < N; i++; i)
    a[i] = b[i] + c[i];
```

Note that the `upc_forall` can contain several statements.



## UPC Job Environment

The SGI UPC run-time environment depends on the SGI Message Passing Toolkit (MPT) MPI and SHMEM libraries and the job launch, parallel job control, memory mapping, and synchronization functionality they provide. UPC jobs are launched like MPT MPI or SHMEM jobs, using the `mpirun(1)` or `mpiexec_mpt(1)` commands. UPC thread numbers correspond to SHMEM PE numbers and MPI rank numbers for `MPI_COMM_WORLD`.

By default, UPC (MPI) jobs have UPC threads (MPI processes) pinned to successive logical CPUs within the system or `cpuset` in which the program is running. This is often optimal, but at times there is benefit in specifying a different mapping of UPC threads to logical CPUs. See the MPI job placement information in the `mpi(1)` man page under **Using a CPU List** and `MPI_DSM_CPULIST`, and see the `omplace(1)` man page for more information about placement of parallel MPI/UPC jobs.

### UPC Quick Start on SGI Altix UV Systems

This section describes environment variable settings that may be appropriate for some common UPC program execution situations.

SGI UPC is designed with two options for performing references to non-local portions of shared arrays:

- Processor driven shared memory
- Global reference unit (GRU) driven shared memory

The GRU is a remote direct memory access (RDMA) facility provided by the UV hub application-specific integrated circuit (ASIC).

By default, UPC uses processor-driven references for nearby sockets and GRU-driven references for more distant references. The threshold between "nearby" and "distant" can be tuned with the `MPI_SHARED_NEIGHBORHOOD` variable, described later in more detail in "UPC Runtime Library Environment Variables" on page 6.

Set the following environment variables:

- Set `MPI_GRU_CBS=0`

This makes all GRU resources available to UPC.

- Some Altix UV systems have Intel processors with two hyper-threads per core, while others have a single hyper-thread per core. When dual hyper-threads per core are available, most HPC codes benefit by leaving one hyper-thread per core idle, thereby, giving more cache and functional unit resources to the active hyper-thread that will be assigned to one of the UPC threads. This is easy to do because the upper half of the logical CPUs (by number) are hyper-threads that are paired with the lower half of the logical CPUs. Set `GRU_RESOURCE_FACTOR=2` when leaving half of the hyper-threads idle.
- You can experiment with the `MPI_SHARED_NEIGHBORHOOD=HOST` variable. Some shared array access patterns will be faster using processor-driven references.
- Set `GRU_TLB_PRELOAD=100` to get the best GRU-based bandwidth for large block copies.

## UPC Runtime Library Environment Variables

The UPC runtime library has a number of environment variables that can affect or tune run-time behavior. They are, as follows:

- `UPC_ALLOC_MAX`

This sets the per-thread maximum amount of memory in bytes that can be allocated dynamically by `upc_alloc()` and the other shared array allocation functions. Note that the `SMA_SYMMETRIC_SIZE` variable needs to be set to the sum of the value of `UPC_ALLOC_MAX` plus the amount of space consumed by statically allocated arrays in the UPC program. See the `intro_shmem(1)` man page for more information about `SMA_SYMMETRIC_SIZE`.

The default is the amount of physical memory per logical CPU on the system.

- `UPC_HEAP_CHECK`

When set to 1, causes `libupc` to check the integrity of the shared memory heap from which shared arrays are allocated.

The default value is 0.

A number of MPI and SHMEM environment variables described on the `MPI(1)`, `SHMEM(1)` and `gru_resource(3)` man pages can be used to tune the execution of UPC programs on SGI Altix UV systems. These man pages should be consulted for a complete list of tunable environment variables. Some of the most helpful variables for UPC programs are, as follows:



- `MPI_SHARED_NEIGHBORHOOD`

This environment variable has an effect only on Altix UV systems. This variable can be set to `HOST` to request that UPC shared arrays use processor-driven shared memory transfers instead of GRU transfers. The size of the memory blocks being accessed in a remote part of a shared array and other factors can determine whether processor-driven or GRU-driven transfers will perform better.

The default setting for the `MPI_SHARED_NEIGHBORHOOD` variable is `BLADE`, which implies that UPC threads will use processor-driven shared memory for references to shared array blocks that have affinity for the threads associated with sockets on the same UV hub.

- `MPI_GRU_CBS` and `MPI_GRU_DMA_CACHESIZE`

These environment variables have an effect only on Altix UV systems. These variables reserve Altix UV GRU resources for MPI and thereby makes them unavailable for UPC. Setting `MPI_GRU_CBS` to 0 will have the result of making all GRU resources available to UPC.

- `GRU_RESOURCE_FACTOR`

This environment variable has an effect only on Altix UV systems. This environment variable specifies an integer multiplier that increases the amount of per-thread GRU resources that can be used by a UPC program. If UPC programs are placed such that some portion of the logical CPUs (hyper-threads) on each UV hub are left idle, you can specify a corresponding multiplier. For example, if half of the logical CPUs are idle, a setting of `GRU_RESOURCE_FACTOR=2` would be recommended. See the `gru_resource(3)` man page for more details.



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